

DESIGN OF THE OPTICAL-ELECTRONIC COMPUTER SYSTEMS FOR IMAGE PROCESSING IN STATIONARY AND DYNAMIC MODES

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ABSTRACT

The theory of designing the optical-electronic image processing computer systems has been presented. A model of parallel image processing system has been considered, that is based on the principle of function decomposition. A structure model of computer system has been examined, that is a conveyor of parallel processors. The implementation possibilities of different image processing operations by optical and electronic computer means have been analyzed. Evaluation of time outlay in the system, while processing an image or a series of them has been made. There have been exposed the dependence of image processing time from conveyor length change and the correlation of optical and electronic devices. The designing method of image processing systems in static mode has been described. The results of investigations of the influence of the median square deviation, of the processing time in the modules on the throughput capacity of the system under the different electronic and optical modules quantity are presented. The recommendations of increasing the system's throughput capacity are formulated. On the bases to these recommendations, the system design method of image processing in the dynamic mode is elaborated.

Keywords: computer system, conveyor, electronic, image, optical, processor

1. INTRODUCTION

Over the last years a significant progress was achieved in the development of special and general - purpose electronic multiprocessor computer systems¹⁻³. Such systems are characterized by a high productivity (up to hundreds billions operations per second). Although they are complicated and consume a significant power that in some cases limits the employment sphere.

The significant results have been obtained in the construction of the optical and optical-electronic processors with acceptable weight - size characteristics and high through - put^{4,5}. High productive processors, which implement different operations of preliminary image processing, image features extraction and invariant pattern recognition have been developed.

However the analysis shows that not all of the image processing operations can be implemented at present by the help of the optical processors. Besides, optical computers are characterized by the limited flexibility. Therefore the construction of high productive efficient computer systems for image processing is connected with the development of multiprocessor systems, which are based on the combination of optical and electronic structures, optimal distribution of the solving problem function among the processing devices of different type.

A number of questions, which are connected with investigation and development of the theory of image processing optical-electronic computer systems design are being presented in this paper.

In section 2 a model of parallel image processing system have been considered. In section 3 the generalized structure of parallel image processing computer system and the implementation possibilities of different image processing operations with the help of optical, electronic computer means are presented. In section 4 there have been made time expenditures in the system and the through-put at the processing of one and series of images. In section 5 there have been presented the method of image processing system design in static mode at different processing devices resources. In section 6 are presented the results of investigations of the influence of the median square deviation, the influence of time of the

processing in the modules on the throughput capacity of the system under the different electronic and optical modules quantity. According to the results of investigations the recommendations of increasing the system's throughput capacity are formulated. On the bases to these recommendations, the system design method of image processing in the dynamic mode is elaborated. An example of image processing computer system design is presented in section 7.

2. MODEL OF PARALLEL IMAGE PROCESSING

Let the image processing problem is determined in the following way: $W\{P(x,y)\} \rightarrow RO$, where W - is the function of image processing; $P(x,y)$ - initial image; RO - the processing result, which can be an image or numerical data.

The most of the image processing problems can be represented in the form of a linear sequence of separate sub problems. The decomposition principle utilization allows to organize parallel processing of two types: in time and in space. Parallel processing in time consists in organization of the sub problems conveyor, which are implemented consecutively.

The spatial concurrency means that on each step of the conveyor processing, the parallel processing of the whole image or its separate fragments is organized. The last type of concurrency is defined by the fact that we can point out some classes of operations while proceeding the image processing. They are executing at each of the image pixels, over separate regions of the image or over the whole image. In other words, to obtain the result we need local (as in the first two cases) or global (as in the last case) initial information. At the working with the local information, the image can be represented as a set of sub images, which are processed by the corresponding set of processor units. The spatial concurrency can be implemented by the help of processor units set of electronic or optical type.

Corresponding with the above-mentioned, the model of the parallel processing of the images can be represented in the following way:

$$RO = C_h\{U[W_i^j(P_{ij})]\},$$

where C_h - the superposition operation, i - the step number of the conveyor; U - the processing results union operation during one of the conveyor steps; j - the image fragment number; W_{ji} - the operation of the processing of the j -th image fragment on i -th step; P_{ij} - the j -th image fragment on i -th step.

This model can be also represented in the form of the operator:

$$P(x,y) \Rightarrow \{W1\}_{M1} \rightarrow \{W2\}_{M2} \rightarrow \dots \rightarrow \{Wk\}_{Mk} \Rightarrow RO.$$

The peculiarity of the presented model consists in the maximum level of disparalleling of image processing operations that ensure the high productivity of the computer system, which implements this model. The productivity also depends on element base which is utilized. In connection to this fact let us look through the generalized structure of parallel image processing computer system and the implementation possibilities of different image processing operations applying optical and electronic computer means.

3. GENERALIZED STRUCTURE OF PARALLEL IMAGE PROCESSING COMPUTER SYSTEM

According to the proposed model, the generalized structure of parallel image processing computer system can be represented as shown in Fig.1, where Y_i - is a device that implements the operation W_i , B_i - buffer device. The device Y_i may be optical or electronic. In the last case it can contain one or a group of processor elements that function in parallel way.

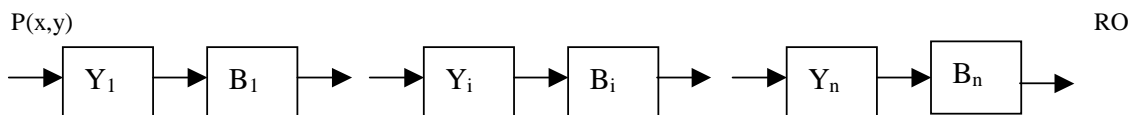


Fig.1. The structure of the image processing computer system