



Influence of annealing in H atmosphere on the electrical properties of Al₂O₃ layers grown on p-type Si by the atomic layer deposition technique

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Abstract

In the present study the electrical properties of 100nm and 400nm alumina films grown by the atomic layer deposition technique on p-type Si before and after a post-deposition annealing at 440°C and after a dc H plasma treatment at different temperatures are investigated. We show that the density of interface states is below $2 \times 10^{10} \text{cm}^{-2}$ in these samples and this value is significantly lower compared to that reported previously in thinner alumina layers (below 50nm). The effective minority carrier lifetime $\tau_{g,eff}$ and the effective surface recombination velocity s_{eff} in untreated p-type Si samples with 100nm and 400nm aluminum oxide is comparable with those obtained after thermal oxidation of 90nm SiO₂. Both, a post-deposition annealing in forming gas (nitrogen/hydrogen) at elevated temperatures and a dc H-plasma treatment at temperatures close to room temperature lead to the introduction of negatively charged defects in alumina films. The results obtained in samples annealed in different atmospheres at different temperatures or subjected to a dc H plasma treatment allow us to correlate these centers with H-related defects. By comparing with theory we tentatively assign them to negatively charged interstitial H atoms.